

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-56 (Cancelled)

57. (New) A method, comprising:

generating a plurality of phase-shifted burst mode signals, and
regulating power to a plurality of loads using respective said phase shifted burst mode signals.

58. (New) The method as claimed in claim 57, further comprising:

generating a PWM signal and wherein power delivered to each respective load determined by the pulse width of said PWM signal.

59. (New) The method as claimed in claim 57, further comprising:

determining a frequency of at least one of said phase-shifted burst mode signals based on a frequency selection signal.

60. (New) The method as claimed in claim 57, wherein each said load comprises a CCFL.

61. (New) The method as claimed in claim 57, further comprising:

generating a plurality of AC signals based at least in part on a respective phase-shifted burst mode signal; and

supplying respective AC signals to respective loads.

62. (New) The method as claimed in claim 57, further comprising:

turning power to at least one load on and off based on, at least in part, the state of a respective phase-shifted burst mode signal.

63. (New) An apparatus, comprising:

an integrated circuit capable of generating a plurality of phase-shifted burst mode signals, said integrated circuit further capable of regulating power to a plurality of loads using respective said phase shifted burst mode signals.

64. (New) The apparatus of claim 63, said integrated circuit further comprising modulator circuitry and phased delay array circuitry, said modulator circuitry is capable of generating a PWM signal having a pulse width, said phased delay array circuitry is capable of receiving said PWM signal and setting a pulse width of at least one said phase-shifted burst mode signal based on the pulse width of said PWM signal.

65. (New) The apparatus of claim 64, said integrated circuit further comprising frequency selector circuitry capable of receiving a reference signal and generating a frequency selection signal based on said reference signal, wherein said phased delay array circuitry is further capable of receiving said frequency selection signal and setting the frequency of said phase-shifted burst mode signals based at least in part on said frequency selection signal.

66. (New) The apparatus of claim 63, said integrated circuit further comprising phase array driver circuitry capable of receiving said plurality of phase-shifted burst mode signals and generating at least one power regulating signal for each respective load.

67. (New) The apparatus of claim 63, wherein said loads comprise a plurality of CCFLs.

68. (New) The apparatus of claim 67, wherein said plurality of CCFLs arranged in an LCD panel.